Data Sheets

Active Serial Memory Interface Data Sheet
Excalibur Device Overview Data Sheet
ByteBlaster II Parallel Port Download Cable Data Sheet
Legacy SDRAM Controller with Avalon Interface Data Sheet
Nios 3.0 CPU Data Sheet
Nios Development Board, Stratix Edition Data Sheet
Nios DMA Data Sheet
Nios PIO Data Sheet
Nios SPI Data Sheet
Nios Timer Data Sheet
Nios Embedded Processor Development Board Data Sheet (APEX Device)
Nios Soft Core Embedded Processor Data Sheet
Nios UART Data Sheet
SDRAM Controller with Avalon Interface Data Sheet
SOPC Builder Data Sheet

Application Notes

AN 141 Excalibur Solutions—Using the SDRAM Controller
AN 142 Excalibur Solutions—Using the Embedded Stripe Bridges
AN 143 Excalibur Solutions—Using the Expansion bus Interface
AN 173 Excalibur Solutions—DPRAM Reference Design
AN 174 Excalibur Solutions—Hello_world.c
AN 177 Excalibur Solutions—Using the Excalibur Stripe PLLs
AN 178 Estimating Nios Resource Usage & Performance in Altera Devices
AN 181 Excalibur Solutions—Multi-Master Reference Design
AN 184 Simultaneous Multi-Mastering with the Avalon Bus
AN 187 Excalibur Solutions—Booting Excalibur Devices
AN 188 Custom Instructions for the Nios Embedded Processor
Excalibur Contents

AN 189  Simulating Nios Embedded Processor Designs
AN 191  Excalibur Solutions—Using the Embedded Stripe Interrupt Controller
AN 192  Excalibur Solutions—Embedded Stripe Performance Designs
AN 196  Excalibur Software Debugging Solutions
AN 224  High-Speed Board Layout Guidelines
AN 244  Using Run-From-Flash Mode with the Excalibur Bootloader
AN 280  Design Verification Using the SignalTap II Embedded Logic Analyzer
AN 283  Simulating Altera Devices with IBIS Models
AN 284  Implementing Interrupt Service Routines in Nios Systems
AN 285  Excalibur Web Server Demonstration
AN 287  Using Excalibur DMA Controllers for Video Imaging and Design Files
AN 298  Reconfiguring Excalibur Devices Under Processor Control
AN 299  System Development Tools for Excalibur Devices

Brochures

Automotive Solutions Brochure
Excalibur Brochure

Errata Sheets

Excalibur EPXA1 Devices Errata Sheet
Excalibur EPXA4 Devices Errata Sheet
Excalibur EPXA10 Devices Errata Sheet
EPXA10 Development Kit Revision 2.1 Errata Sheet

General Information

About this CD-ROM
How to Contact Altera
Legal Notice
Ordering Information
PLL & Timing Glossary

Selector Guides

Component Selector Guide
User Guides & Software Manuals

- ARM-Based Embedded Processor PLDs Hardware Reference Manual
- ARM-Based Hardware Design Tutorial
- EPXA1 Development Board Hardware Reference Manual
- EPXA1 Development Kit Getting Started User Guide
- EPXA10 Development Kit Getting Started User Guide
- EPXA10 DDR Development Kit Getting Started User Guide
- Excalibur Bus Functional Model User Guide
- Excalibur Stripe Simulator User Guide
- GNUPro Toolkit User’s Guide for ARM and ARM/Thumb Development
- Nios Development Kit, Stratix Edition Getting Started User Guide
- Nios Embedded Processor Getting Started User Guide
- Nios Embedded Processor Peripherals Reference Manual
- Nios Ethernet Development Kit User Guide
- PCI32 Nios Target MegaCore Function User Guide
- Plugs Ethernet Library Reference Manual

White Papers

- Excalibur Embedded Processor PLD Stripe Power Consumption White Paper
- Co-Simulation of Embedded Systems Implemented in FPGAs White Paper
- OpenCore Plus Hardware Evaluation with the Excalibur Solutions White Paper