

Table of Contents

Invited Talks

Software Analysis and Model Checking	1
<i>Gerard J. Holzmann</i>	
The Quest for Efficient Boolean Satisfiability Solvers	17
<i>Lintao Zhang and Sharad Malik</i>	

Invited Tutorials

On Abstraction in Software Verification	37
<i>Patrick Cousot and Radhia Cousot</i>	
The Symbolic Approach to Hybrid Systems	57
<i>Thomas A. Henzinger</i>	
Infinite Games and Verification (Extended Abstract of a Tutorial)	58
<i>Wolfgang Thomas</i>	

Symbolic Model Checking

Symbolic Localization Reduction with Reconstruction Layering and Backtracking	65
<i>Sharon Barner, Daniel Geist, and Anna Gringauze</i>	
Modeling and Verifying Systems Using a Logic of Counter Arithmetic with Lambda Expressions and Uninterpreted Functions	78
<i>Randal E. Bryant, Shuvendu K. Lahiri, and Sanjit A. Seshia</i>	
Combining Symmetry Reduction and Under-Approximation for Symbolic Model Checking	93
<i>Sharon Barner and Orna Grumberg</i>	

Abstraction/Refinement and Model Checking

Liveness with $(0, 1, \infty)$ -Counter Abstraction	107
<i>Amir Pnueli, Jessie Xu, and Lenore Zuck</i>	
Shared Memory Consistency Protocol Verification Against Weak Memory Models: Refinement via Model-Checking	123
<i>Prosenjit Chatterjee, Hemanthkumar Sivaraj, and Ganesh Gopalakrishnan</i>	
Automatic Abstraction Using Generalized Model Checking	137
<i>Patrice Godefroid and Radha Jagadeesan</i>	

Compositional/Structural Verification

Property Checking via Structural Analysis	151
<i>Jason Baumgartner, Andreas Kuehlmann, and Jacob Abraham</i>	
Conformance Checking for Models of Asynchronous Message Passing Software	166
<i>Sriram K. Rajamani and Jakob Rehof</i>	
A Modular Checker for Multithreaded Programs	180
<i>Cormac Flanagan, Shaz Qadeer, and Sanjit A. Seshia</i>	

Timing Analysis

Automatic Derivation of Timing Constraints by Failure Analysis	195
<i>Tomohiro Yoneda, Tomoya Kitai, and Chris Myers</i>	
Deciding Separation Formulas with SAT	209
<i>Ofer Strichman, Sanjit A. Seshia, and Randal E. Bryant</i>	
Probabilistic Verification of Discrete Event Systems Using Acceptance Sampling	223
<i>Håkan L. S. Younes and Reid G. Simmons</i>	

SAT Based Methods

Checking Satisfiability of First-Order Formulas by Incremental Translation to SAT	236
<i>Clark W. Barrett, David L. Dill, and Aaron Stump</i>	
Applying SAT Methods in Unbounded Symbolic Model Checking	250
<i>Ken L. McMillan</i>	
SAT Based Abstraction-Refinement Using ILP and Machine Learning Techniques	265
<i>Edmund Clarke, Anubhav Gupta, James Kukula, and Ofer Strichman</i>	
Semi-formal Bounded Model Checking	280
<i>Jesse D. Bingham and Alan J. Hu</i>	

Symbolic Model Checking

Algorithmic Verification of Invalidation-Based Protocols	295
<i>Marco Bozzano and Giorgio Delzanno</i>	
Formal Verification of Complex Out-of-Order Pipelines by Combining Model-Checking and Theorem-Proving	309
<i>Christian Jacobi</i>	

Automated Unbounded Verification of Security Protocols	324
<i>Yannick Chevalier and Laurent Vigneron</i>	

Tool Presentations

Exploiting Behavioral Hierarchy for Efficient Model Checking	338
<i>Rajeev Alur, Michael McDougall, and Zijiang Yang</i>	
IF-2.0: A Validation Environment for Component-Based Real-Time Systems	343
<i>Marius Bozga, Susanne Graf, and Laurent Mounier</i>	
The AVISS Security Protocol Analysis Tool	349
<i>Alessandro Armando, David Basin, Mehdi Bouallagui, Yannick Chevalier, Luca Compagna, Sebastian Mödersheim, Michael Rusinowitch, Mathieu Turuani, Luca Viganò, and Laurent Vigneron</i>	
SPeeDI – A Verification Tool for Polygonal Hybrid Systems	354
<i>Eugene Asarin, Gordon Pace, Gerardo Schneider, and Sergio Yovine</i>	
NuSMV 2: An OpenSource Tool for Symbolic Model Checking	359
<i>Alessandro Cimatti, Edmund Clarke, Enrico Giunchiglia, Fausto Giunchiglia, Marco Pistore, Marco Roveri, Roberto Sebastiani, and Armando Tacchella</i>	
The d/dt Tool for Verification of Hybrid Systems	365
<i>Eugene Asarin, Thao Dang, and Oded Maler</i>	

Infinite State Model Checking

Model Checking Linear Properties of Prefix-Recognizable Systems	371
<i>Orna Kupferman, Nir Piterman, and Moshe Y. Vardi</i>	
Using Canonical Representations of Solutions to Speed Up Infinite-State Model Checking	386
<i>Tatiana Rybina and Andrei Voronkov</i>	
On Discrete Modeling and Model Checking for Nonlinear Analog Systems	401
<i>Walter Hartong, Lars Hedrich, and Erich Barke</i>	

Compositional/Structural Verification

Synchronous and Bidirectional Component Interfaces	414
<i>Arindam Chakrabarti, Luca de Alfaro, Thomas A. Henzinger, and Freddy Y. C. Mang</i>	

Interface Compatibility Checking for Software Modules 428
*Arindam Chakrabarti, Luca de Alfaro, Thomas A. Henzinger,
Marcin Jurdziński, and Freddy Y.C. Mang*

Practical Methods for Proving Program Termination 442
Michael A. Colón and Henny B. Sipma

Extended Model Checking

Evidence-Based Model Checking 455
Li Tan and Rance Cleaveland

Mixing Forward and Backward Traversals
in Guided-Prioritized BDD-Based Verification 471
Gianpiero Cabodi, Sergio Nocco, and Stefano Quer

Vacuum Cleaning CTL Formulae 485
Mitra Purandare and Fabio Somenzi

Tool Presentations

CVC: A Cooperating Validity Checker 500
Aaron Stump, Clark W. Barrett, and David L. Dill

χ Chek: A Multi-valued Model-Checker 505
Marsha Chechik, Arie Gurfinkel, and Benet Devereux

PathFinder: A Tool for Design Exploration 510
*Shoham Ben-David, Anna Gringauze, Baruch Sterin,
and Yaron Wolfsthal*

Abstracting C with abC 515
Dennis Dams, William Hesse, and Gerard Holzmann

AMC: An Adaptive Model Checker 521
Alex Groce, Doron Peled, and Mihalis Yannakakis

Code Verification

Temporal-Safety Proofs for Systems Code 526
*Thomas A. Henzinger, Ranjit Jhala, Rupak Majumdar,
George C. Necula, Grégoire Sutre, and Westley Weimer*

Regular Model Checking and Acceleration

Extrapolating Tree Transformations 539
Ahmed Bouajjani and Tayssir Touili

Regular Tree Model Checking	555
<i>Parosh Aziz Abdulla, Bengt Jonsson, Pritha Mahata, and Julien d’Orso</i>	
Compressing Transitions for Model Checking	569
<i>Robert Kurshan, Vladimir Levin, and Hüsnü Yenigün</i>	
 Model Reduction	
Canonical Prefixes of Petri Net Unfoldings	582
<i>Victor Khomenko, Maciej Koutny, and Walter Vogler</i>	
State Space Reduction by Proving Confluence	596
<i>Stefan Blom and Jaco van de Pol</i>	
Fair Simulation Minimization	610
<i>Sankar Gurumurthy, Roderick Bloem, and Fabio Somenzi</i>	
Author Index	625



<http://www.springer.com/978-3-540-43997-4>

Computer Aided Verification

14th International Conference, CAV 2002 Copenhagen,

Denmark, July 27-31, 2002 Proceedings

Brinksma, E.; Larsen, K.G. (Eds.)

2002, X, 362 p., Softcover

ISBN: 978-3-540-43997-4