

Preface

Welcome to the proceedings of PATMOS 2004, the fourteenth in a series of international workshops. PATMOS 2004 was organized by the University of Patras with technical co-sponsorship from the IEEE Circuits and Systems Society.

Over the years, the PATMOS meeting has evolved into an important European event, where industry and academia meet to discuss power and timing aspects in modern integrated circuit and system design. PATMOS provides a forum for researchers to discuss and investigate the emerging challenges in design methodologies and tools required to develop the upcoming generations of integrated circuits and systems. We realized this vision this year by providing a technical program that contained state-of-the-art technical contributions, a keynote speech, three invited talks and two embedded tutorials. The technical program focused on timing, performance and power consumption, as well as architectural aspects, with particular emphasis on modelling, design, characterization, analysis and optimization in the nanometer era.

This year a record 152 contributions were received to be considered for possible presentation at PATMOS. Despite the choice for an intense three-day meeting, only 51 lecture papers and 34 poster papers could be accommodated in the single-track technical program. The Technical Program Committee, with the assistance of additional expert reviewers, selected the 85 papers to be presented at PATMOS and organized them into 13 technical sessions. As was the case with the PATMOS workshops, the review process was anonymous, full papers were required, and several reviews were received per manuscript.

Beyond the presentations of the papers, the PATMOS technical program was enriched by a series of speeches offered by world-class experts, on important emerging research issues of industrial relevance. This year's keynote speech was offered by Prof. Hugo De Man, IEEE Fellow, IMEC and KUL, Belgium. The title of the keynote speech was "Connecting E-Dreams to Deep-Submicron Realities." Furthermore, the technical program was augmented with three invited talks, given by leading industry experts, namely Dr. Nick Kanopoulos, Director of Multimedia Communications, ATMEL, USA, Prof. Kiyo Itoh, Hitachi Fellow, Hitachi, Japan and Dr. Carlo Dallavalle, Telecom Group Process Technology Director, STMicroelectronics, Agrate Brianza, Italy. Finally, two tutorials were embedded within the technical program on two very hot emerging design issues. In particular, Prof. W. Nebel, OFFIS, Germany, gave a tutorial on the "Importance and Sources of Leakage Power in DSM-CMOS Circuits," while Sachin S. Sapatnekar, University of Minnesota, USA, presented "The Certainty of Uncertainty: Randomness in Nanometer Design."

Last, but not least, the rich social program coupled with the unique Santorini settings provided the additional opportunities for establishing new relationships

among the workshop participants. We sincerely hope that the attendees found PATMOS 2004 at Santorini a stimulating and fruitful experience and that they enjoyed their stay on Santorini throughout the duration of the event. We would like to thank the several people who voluntarily worked to make this edition of PATMOS possible, the expert reviewers, the members of the technical program and steering committees, and the invited speakers who offered their skill, time, and deep knowledge to make PATMOS 2004 a memorable event.

September 2004

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Integrated Circuit and System Design
Power and Timing Modeling, Optimization and
Simulation; 14th International Workshop, PATMOS 2004,
Santorini, Greece, September 15-17, 2004,
Proceedings
Macii, E.; Paliouras, V.; Koufopavlou, O. (Eds.)
2004, XVI, 916 p., Softcover
ISBN: 978-3-540-23095-3