

Contents

Part I Applications

High-Performance Hardware Acceleration of Asset Simulations	3
Christian de Schryver, Henning Marxen, Stefan Weithoffer, and Norbert Wehn	
Monte-Carlo Simulation-Based Financial Computing on the Maxwell FPGA Parallel Machine	33
Xiang Tian and Khaled Benkrid	
Bioinformatics Applications on the FPGA-Based High-Performance Computer RIVYERA	81
Lars Wienbrandt	
FPGA-Accelerated Molecular Dynamics	105
M.A. Khan, M. Chiu, and M.C. Herbordt	
FPGA-Based HPRC for Bioinformatics Applications.....	137
Yoshiki Yamaguchi, Yasunori Osana, Masato Yoshimi, and Hideharu Amano	
High-Performance Computing for Neuroinformatics Using FPGA	177
Will X.Y. Li, Rosa H.M. Chan, Wei Zhang, Chiwai Yu, Dong Song, Theodore W. Berger, and Ray C.C. Cheung	
High-Performance FPGA-Accelerated Real-Time Search	209
Wim Vanderbauwhede, Sai. R. Chalamalasetti, and Martin Margala	
High-Performance Data Processing Over <i>N</i>-ary Trees	245
Valery Sklyarov and Iouliia Skliarova	
FPGA-Based Systolic Computational-Memory Array for Scalable Stencil Computations	279
Kentaro Sano	

High Performance Implementation of RTM Seismic Modeling on FPGAs: Architecture, Arithmetic and Power Issues	305
Victor Medeiros, Abner Barros, Abel Silva-Filho, and Manoel E. de Lima	
High-Performance Cryptanalysis on RIVYERA and COPACOBANA Computing Systems	335
Tim Güneysu, Timo Kasper, Martin Novotný, Christof Paar, Lars Wienbrandt, and Ralf Zimmermann	
FPGA-Based HPRC Systems for Scientific Applications	367
Tsuyoshi Hamada and Yuichiro Shibata	
Accelerating the SPICE Circuit Simulator Using an FPGA: A Case Study	389
Nachiket Kapre and André DeHon	
 Part II Architectures	
The Convey Hybrid-Core Architecture	431
Bernd Klauer	
Low Cost High Performance Reconfigurable Computing	453
Javier Castillo, Jose Luis Bosque, Cesar Pedraza, Emilio Castillo, Pablo Huerta, and Jose Ignacio Martinez	
An FPGA-Based Supercomputer for Statistical Physics: The Weird Case of Janus	481
M. Baity-Jesi, R.A. Baños, A. Cruz, L.A. Fernandez, J.M. Gil-Narvion, A. Gordillo-Guerrero, M. Guidetti, D. Iñiguez, A. Maiorano, F. Mantovani, E. Marinari, V. Martin-Mayor, J. Monforte-Garcia, A. Muñoz Sudupe, D. Navarro, G. Parisi, M. Pivanti, S. Perez-Gaviro, F. Ricci-Tersenghi, J.J. Ruiz-Lorenzo, S.F. Schifano, B. Seoane, A. Tarancon, P. Tellez, R. Tripiccion, and D. Yllanes	
Accelerate Communication, not Computation!	507
Mondrian Nüssle, Holger Fröning, Sven Kapferer, and Ulrich Brüning	
High-Speed Torus Interconnect Using FPGAs	543
H. Baier, S. Heybrock, B. Krill, F. Mantovani, T. Maurer, N. Meyer, I. Ouda, M. Pivanti, D. Pleiter, S.F. Schifano, and H. Simma	
MEMSCALE: Re-architecting Memory Resources for Clusters	569
Holger Fröning, Federico Silla, and Hector Montaner	

High-Performance Computing Based on High-Speed Dynamic Reconfiguration	605
Minoru Watanabe	
 Part III Tools and Methodologies	
Reconfigurable Arithmetic for High-Performance Computing	631
Florent de Dinechin and Bogdan Pasca	
Acceleration of the Discrete Element Method: From RTL to C-Based Design	665
Benjamin Carrion Schafer and Kazutoshi Wakabayashi	
Optimising Euroben Kernels on Maxwell	695
James Perry, Mark Parsons, and Paul Graham	
Assessing Productivity of High-Level Design Methodologies for High-Performance Reconfigurable Computers	719
Esam El-Araby, Saumil G. Merchant, and Tarek El-Ghazawi	
Maximum Performance Computing with Dataflow Engines	747
Oliver Pell, Oskar Mencer, Kuen Hung Tsoi, and Wayne Luk	
Index	775

<http://www.springer.com/978-1-4614-1790-3>

High-Performance Computing Using FPGAs

Vanderbauwhede, W.; Benkrid, K. (Eds.)

2013, XI, 803 p. 420 illus., 232 illus. in color., Hardcover

ISBN: 978-1-4614-1790-3