

Contents

1	SSD Market Overview	1
	G. Wong	
2	SSD Architecture and PCI Express Interface	19
	K. Eshghi and R. Micheloni	
3	SAS and SATA SSDs	47
	S. Yasarapu	
4	Hybrid Storage.....	61
	R. Micheloni, L. Crippa, and M. Picca	
5	NAND Flash Technology	79
	M.F. Beug	
6	NAND Flash Design	127
	L. Crippa and R. Micheloni	
7	NAND and Controller Co-design for SSDs	173
	K. Takeuchi	
8	SSD Reliability	203
	C. Zambelli and P. Olivo	
9	Efficient Wear Leveling in NAND Flash Memory	233
	Yuan-Hao Chang and Li-Pin Chang	
10	BCH for Solid-State-Drives.....	259
	A. Marelli and R. Micheloni	
11	Low-Density Parity-Check (LDPC) Codes	293
	E. Paolini	

12 Protecting SSD Data Against Attacks 333
A. Marelli and R. Micheloni

13 Flash Signal Processing and NAND/ReRAM SSD 357
K. Takeuchi

Index 375

Inside Solid State Drives (SSDs)

Micheloni, R.; Marelli, A.; Eshghi, K.

2013, XVIII, 382 p., Hardcover

ISBN: 978-94-007-5145-3