

Contents

Challenges in Embedded Engineering Education	1
Ivan Kastelan, Nikola Teslic and Miodrag Temerinac	
Unified Learning Platform for Embedded Engineering	29
Ivan Kastelan, Nikola Teslic and Miodrag Temerinac	
Exercises for Embedded Engineering Learning Platform	45
Branka Medved Rogina, Karolj Skala, Peter Škoda, Ivan Sović and Ivan Michieli	
Implementation of Advanced Historical Computer Architectures.	61
Zorislav Šojat, Karolj Skala, Branka Medved Rogina, Peter Škoda and Ivan Sović	
Methods for User Involvement in the Design of Augmented Reality Systems for Engineering Education.	81
Margarita Anastassova, Sabrina Panëels and Florent Souvestre	
Augmented Reality Interface for E2LP: Assistance in Electronic Laboratories Through Augmented Reality.	93
Enara Artetxe González, Florent Souvestre and Jorge R. López Benito	
E2LP Remote Laboratory: e-Learning Service for Embedded Systems Education	109
Rafał Kłoda and Jan Piwiński	
Advanced Projects and Applications for Embedded Systems Engineering on E2LP Platform	119
Dario Grgić, Sebastian Böttcher, Marc Pfeifer, Johannes Scherle, Benjamin Völker, Jan Burchard, Sebastian Sester and Leonhard M. Reindl	
E2LP Remote Laboratory: Introduction Course and Evaluation at Warsaw University of Technology.	133
Rafał Kłoda, Jan Piwiński and Roman Szewczyk	

Exploring Aspects of Self-regulated Learning Among Engineering Students Learning Digital System Design in the FPGA Environment—Methodology and Findings	139
Moshe Barak, Ivan Kastelan and Zvi Azia	
Is It Possible to Increase Motivation for Study Among Sophomore Electrical and Computer Engineering Students?	161
Aharon Gero	
Interrupts Become Features: Using On-Sensor Intelligence for Recognition Tasks	171
Kristof Van Laerhoven and Philipp M. Scholl	

Embedded Engineering Education

Szewczyk, R.; Kaštalan, I.; Temerinac, M.; Barak, M.;

Sruk, V. (Eds.)

2016, XII, 185 p. 78 illus., 12 illus. in color., Softcover

ISBN: 978-3-319-27539-0