

# Preface

The authors of this book all worked together at the Eindhoven University of Technology in the Netherlands. They were united in what was unofficially called the “Memory Team,” as either a Ph.D. student, an assistant professor, or a professor. The team worked on various challenging research topics in the context of memory controllers for real-time embedded systems, which matched well with the overall goals of the Electronic Systems group by which they were all hosted. The authors thank the other Ph.D. students in the Memory Team, Manil Gomony and Yonghui Li, for their valuable input during countless discussions over the years, and for all the fun that was had in the process. A large portion of the preliminary exploration work for the topics discussed in this book was done by two excellent master students, Tim Kouters and Jasper Kuijsten. They were great to have around, and delivered good work, for which the authors are grateful.

A memory controller requires a system that tells it what to do. For the controller in this book, the system takes the form of the CompSOC platform. The various hardware and software components it consists of were jointly maintained by Eindhoven University of Technology and Delft University of Technology. Most of the experiments in this book would not have been possible without the infrastructure created by current and past CompSOC team members. The authors would particularly like to thank Anca Molnos, Andrew Nelson, Ashkan Beyranvand Nejad, Davit Mirzoyan, Gabriela Breaban, Juan Valencia, Martijn Koedam, Radu Stefan, Rasool Tavakoli, Reinier van Kampenhout, and Shubhendu Sinha for their work, and the great company they are.

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Architectures, Methodologies and Trade-offs

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