

# Preface

This volume contains the papers presented at ATVA 2016, the 14th International Symposium on Automated Technology for Verification and Analysis held during October 17–20 in Chiba, Japan. The purpose of ATVA is to promote research on theoretical and practical aspects of automated analysis, verification, and synthesis by providing an international forum for interaction among researchers in academia and industry.

ATVA attracted 82 submissions in response to the call for papers. Each submission was assigned to at least four reviewers of the Program Committee. The Program Committee discussed the submissions electronically, judging them on their perceived importance, originality, clarity, and appropriateness to the expected audience. The Program Committee selected 31 papers for presentation, leading to an acceptance rate of 38 %.

The program of ATVA also included three invited talks and three invited tutorial given by Prof. Tevfik Bultan (University of Santa Barbara), Prof. Javier Esparza (Technical University of Munich), and Prof. Masahiro Fujita (Fujita Laboratory and the University of Tokyo).

The chairs would like to thank the authors for submitting their papers to ATVA 2016. We are grateful to the reviewers who contributed to nearly 320 informed and detailed reports and discussions during the electronic Program Committee meeting. We also sincerely thank the Steering Committee for their advice. Finally, we would like to thank the local organizers, Prof. Mitsuharu Yamamoto and Prof. Yoshinori Tanabe, who devoted a large amount of their time to the conference. ATVA received financial help from the National Institute of Advanced Industrial Science and Technology (AIST), Springer, Chiba Convention Bureau and International Center (CCB-IC), and Chiba University.

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