

Contents

1	The Business of NAND.	1
	Rahul N. Advani	
2	Reliability of 3D NAND Flash Memories.	29
	A. Grossi, C. Zambelli and P. Olivo	
3	3D Stacked NAND Flash Memories	63
	Rino Micheloni and Luca Crippa	
4	3D Charge Trap NAND Flash Memories	85
	Luca Crippa and Rino Micheloni	
5	3D Floating Gate NAND Flash Memories	129
	Rino Micheloni and Luca Crippa	
6	Advanced Architectures for 3D NAND Flash Memories with Vertical Channel	167
	Luca Crippa and Rino Micheloni	
7	3D VG-Type NAND Flash Memories	197
	Andrea Silvagni	
8	RRAM Cross-Point Arrays.	223
	Huaqiang Wu, Yan Liao, Bin Gao, Debanjan Jana and He Qian	
9	3D Multi-chip Integration and Packaging Technology for NAND Flash Memories	261
	Herb Huang and Rino Micheloni	
10	BCH and LDPC Error Correction Codes for NAND Flash Memories	281
	Alessia Marelli and Rino Micheloni	
11	Advanced Algebraic and Graph-Based ECC Schemes for Modern NVMs	321
	Frederic Sala, Clayton Schoeny and Lara Dolecek	

12 System-Level Considerations on Design of 3D NAND
 Flash Memories 349
 Chao Sun and Ken Takeuchi

Index 377

<http://www.springer.com/978-94-017-7510-6>

3D Flash Memories

Micheloni, R. (Ed.)

2016, XXII, 380 p. 396 illus., 304 illus. in color.,

Hardcover

ISBN: 978-94-017-7510-6