

Contents

1	Digital Right Management for IP Protection.	1
	Lionel Torres, Pascal Benoit, Jérôme Rampon, Renaud Perillat, Dominic Spring, Gael Paul, Stephane Bonniol and Lilian Bossuet	
2	Turning Electronic Circuits Features into On-Chip Locks.	15
	Brice Colombier, Lilian Bossuet and David Hély	
3	Logic Modification-Based IP Protection Methods: An Overview and a Proposal	37
	Brice Colombier, Lilian Bossuet and David Hély	
4	IP FSM Watermarking.	65
	Edward Jung and Lilian Bossuet	
5	Side Channel Analysis, an Efficient Ally for IP Protection.	85
	Lilian Bossuet and Cédric Marchand	
6	Hardware Obfuscation: Techniques and Open Challenges	105
	Georg T. Becker, Marc Fyrbiak and Christian Kison	
7	An Application of Partial Hardware Reverse Engineering for the Detection of Hardware Trojan.	125
	Franck Courbon	
8	Linear Complementary Codes: Novel Hardware Trojan Prevention and Detection Approach	149
	Xuan Thuy Ngo, Sylvain Guilley and Jean-Luc Danger	
9	Ultra-Lightweight Implementation in Area of Block Ciphers	177
	Cédric Marchand, Lilian Bossuet and Kris Gaj	
10	Enhancing Secure Elements—Technology and Architecture	205
	Bertrand Cambou	
	Index	233

<http://www.springer.com/978-3-319-50378-3>

Foundations of Hardware IP Protection

Bossuet, L.; Torres, L. (Eds.)

2017, VII, 240 p. 125 illus., 48 illus. in color., Hardcover

ISBN: 978-3-319-50378-3