

# Preface

This volume contains the papers presented at RC 2017, the 9th International Conference on Reversible Computation, held during July 6–7, 2017, in Kolkata (India). The conference was jointly organized by: (a) the Indian Institute of Technology, Kharagpur, (b) the Indian Institute of Engineering Science and Technology, Shibpur, (c) the Indian Statistical Institute, Kolkata, and (d) Jadavpur University, Kolkata.

Reversible computation has a growing number of promising application areas such as low-power design, testing and verification, database recovery, discrete event simulation, reversible specification formalisms, reversible programming languages, process algebras, quantum computation, etc. RC 2017 was the ninth event in a series of annual meetings designed to gather researchers from different scientific disciplines for the discussion and dissemination of recent developments in all aspects of reversible computation. Previous RC events took place in York, UK (2009), Bremen, Germany (2010), Ghent, Belgium (2011), Copenhagen, Denmark (2012), Victoria, Canada (2013), Kyoto, Japan (2014), Grenoble, France (2015), and Bologna, Italy (2016).

The RC 2017 conference included two invited talks, by Kalyan S. Perumalla (abstract included in the front matter of this volume) and Martin Roetteler (invited paper included in this volume). The conference received 47 valid submissions, all of which were reviewed by at least three members of the Program Committee. After careful deliberation, the Program Committee selected 13 full papers and five short papers for inclusion in these proceedings and for presentation at the conference.

We would like to thank everyone who contributed to the success of RC 2017: the authors for submitting the results of their research to RC 2017; our two invited speakers for their inspiring talks and their time; the Program Committee and their co-reviewers for their expert work under tight time constraints, and for their commitment to the quality of the conference; the general chairs, Indranil Sengupta and Rolf Drechsler, for keeping the conference on track and for their help and advice; the organizing chair, Amlan Chakrabarti; the student volunteers for their untiring effort to ensure the smooth running of the conference; and the attendees of the events for their interest in the presentations and the constructive discussions.

We would like to thank our sponsors for supporting this conference: VLSI Society of India, Nucleodyne Systems, USA, and JIS Group Educational Initiatives.

We benefited from using the EasyChair system for the work of the Program Committee and the compilation of the proceedings.

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