

## Part III

# Quantum Wires

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In the following Sections, experiments on single semiconductor quantum wires, lateral superlattices, and semiconductor rings performed and published since the outset of this very active research area are briefly described. The order in which the descriptions of the experiments are given is according to the submission dates of the corresponding publications.

Each section begins with a brief introduction into the phenomenon or the phenomena described in that section. The references given are not meant to represent an overview of the available theoretical literature. The cited publications contain additional references on the theoretical background, though. The main theoretical results on the theory of quantum transport are summarized in Section 12 (written by the editor).

In nearly all of the experiments described here, more than one question was addressed. Hence, a given experiment is described once as a whole in one of the Sections. In another Section also related to that particular experiment, a short reference is given including the number of the page where the complete description may be found.

## 6 Overview over systems

Semiconductor quantum wires may be defined using a large number of different materials and fabrication techniques. A very brief overview of the materials and the techniques used in the experiments described in the following chapters is given here.

### 6.1 Si-based systems

Si-based systems (see for example [82A, 91F1, 96S2, 98T2] and references therein) have been employed in order to study the transport properties of single wires (first reported by Fowler et al in [82F], see page 125), lateral superlattices (first reported by Skocpol et al in [82S1], see page 228) and connected rings (first reported by Gao et al in [94G1], see page 272). preparation and characterization may be found in the conference proceedings [80F, 84B1, 84C, 86B1, 86K1, 86V, 87D, 87L1, 88G1, 88L1, 88M1, 89A, 89D1, 89F1, 89L1, 89M1, 89N, 90B1, 90F1, 90K2, 91C, 91K1, 92B1, 92M1, 92P1, 93L1, 93L2, 93S, 94G2, 94I1, 95A1, 95C, 95E1, 95F1, 96B1, 96J1, 96K1, 97A1, 97S1, 97S2, 98G1].

### 6.2 AlGaAs/GaAs-heterostructures

AlGaAs/GaAs-heterostructures (see for example [82A, 91F1, 91W1, 96S2] and references therein) have been used in order to measure the transport properties of single wires (first reported by Choi et al in [85C], see page 168), lateral superlattices (first reported by Brinkop et al in [88B1], see page 244), isolated rings (first reported by Mailly et al in [93M1, 94M1], see page 252), and connected rings (first reported by Timp et al in [87T1], see page 264). on materials, preparation and characterization may be found in the conference proceedings [80F, 84B1, 84C, 86B1, 86K1, 86V, 87D, 87L1, 88G1, 88L1, 88M1, 89A, 89D1, 89F1, 89L1, 89M1, 89N, 90B1, 90F1, 90K2, 91C,

91K1, 92B1, 92M1, 92P1, 93L1, 93L2, 93S, 94G2, 94I1, 95A1, 95C, 95E1, 95F1, 96B1, 96J1, 96K1, 97A1, 97S1, 97S2, 98G1].

### 6.3 Quantum wires based on other III-V and II-VI materials

Transport in single wires has been examined in systems of InGaAs/InP (first reported by Menschig et al in [90M1, 90M2], see page 116), InAs/AlGaSb (first reported by Haug et al in [92H1], see page 180), HgCdMnTe and PbMnTe (first reported by Dietl et al in [93D], see page 191, for information on the material see [88F1], and CdMnTe (first reported by Jaroszyński et al in [95J], see page 181, see also [88F1]). Transport properties of lateral superlattices have been measured in systems of InSb (first reported by Alsmeier et al in [88A], see page 244), InGaAs/InAlAs/InP (first reported by Kern et al in [90K1], see page 225), AlGaAs/InGaAs/GaAs (first reported by Carpi et al in [93C1], see page 243), InGaAs/InP (first reported by Kreschuk et al in [94K1], see page 289), InAs/AlGaSb (first reported by Sasa et al in [96S1], see page 226), and  $(\text{InP})_2/(\text{GaP})_2$  (first reported by Tang et al in [98T1], see page 229). investigated in systems of GaInAs/AlInAs (first reported by Kurdak et al in [92K1], see page 256), InO (first reported by Chandrasekhar et al in [94C1], see page 275), InGaAs/InP (first reported by Appenzeller et al in [95A2], see page 258), and InGaAs/AlGaAs (first reported by Bykov et al in [96B2], see page 291). Additional information on materials, preparation and characterization may be found in the conference proceedings [80F, 84B1, 84C, 86B1, 86K1, 86V, 87D, 87L1, 88G1, 88L1, 88M1, 89A, 89D1, 89F1, 89L1, 89M1, 89N, 90B1, 90F1, 90K2, 91C, 91K1, 92B1, 92M1, 92P1, 93L1, 93L2, 93S, 94G2, 94I1, 95A1, 95C, 95E1, 95F1, 96B1, 96J1, 96K1, 97A1, 97S1, 97S2, 98G1].

### 6.4 Preparation and structuring

Semiconductor heterostructures are usually grown by *molecular beam epitaxy* (MBE) or *metal organic vapour phase epitaxy* (MOVPE) (see for example [82S2, 84C, 85J, 91F1, 91W1, 92P1, 94T, 96S2] and references therein). In order to define a specific structure, a layer of resist is spun onto the wafer. Then, *optical lithography*, *holographic lithography*, *electron beam lithography* (EBL), and *focused ion beam* (FIB) *lithography* may be employed in order to write the pattern into the resist (see for example [88H1, 90B1, 91F1, 91W1, 92P1, 92V, 94G2, 94T, 96S2, 97F] and references therein). The resist is developed and is either washed away where it was exposed (positive resist) or where it was not exposed (negative resist) (see for example [92P1] and references therein).

Different processing steps may follow. First, a thin metal film may be evaporated onto the patterned and developed resist. When the resist is dissolved away, the metal film only remains in places where the resist was washed away during development. This process is called *lift-off* and is used to deposit metallic gates and contacts (see for example [88H1, 90B1, 91W1, 92P1, 96S2, 97F, 98T2] and references therein). Second, the resist pattern may be used as a mask for selective etching (affecting the bare semiconductor but not the resist). One uses either *wet chemical etching* or *dry etching* including *plasma etching*, *reactive ion etching* (RIE), and *reactive ion beam etching* (see for example [88H1, 90B1, 91F1, 91W1, 92P1, 96S2, 97F, 98T2] and references therein).

Alternatives to the above fabrication techniques are, for example, implantation to form local *p-n* junctions (see for example [98T2] and references therein), direct writing of the pattern into the semiconductor using *focused ion beam implantation* (see for example [91F1, 92P1, 96S2, 98T2] and references therein) or *cleaved edge overgrowth* where two orthogonal quantum wells form a T-shaped wire (see for example [92S1, 97A1] and references therein).

Additional information on materials, preparation and characterization may be found in the conference proceedings [80F, 84B1, 84C, 86B1, 86K1, 86V, 87D, 87L1, 88G1, 88L1, 88M1, 89A, 89D1, 89F1, 89L1, 89M1, 89N, 90B1, 90F1, 90K2, 91C, 91K1, 92B1, 92M1, 92P1, 93L1, 93L2, 93S, 94G2, 94I1, 95A1, 95C, 95E1, 95F1, 96B1, 96J1, 96K1, 97A1, 97S1, 97S2, 98G1].

## 6.5 Characterization

A sample is characterized by its spatial dimensions, i. e. *thickness*  $t$ , *width*  $w$  and *length*  $L$ . The amount of disorder in a sample determines the *mean free path*  $l$ , which is related to the *elastic scattering time*  $\tau$  and the *Fermi velocity* of the electrons  $v_F$  by  $l = v_F \tau$ . When electrons are accelerated in an electric field  $E$ , they assume a *drift velocity*  $v_D = -eE\tau/m^*$ , where  $m^*$  is the *effective mass* and  $e\tau/m^*$  is the *drift electron mobility*  $\mu_D$ . The mobility together with the *carrier concentration*  $n$  yields the *Drude conductivity*,  $\sigma = en\mu_D$ . The product of the Hall resistance,  $R_H$ , and the conductivity is called *Hall mobility*,  $\mu_H = |R_H\sigma|$ . The *diffusion constant*  $D = v_F^2\tau/2$  is a measure of the area in which an electron may be found after it has performed many elastic scattering events (see for example [81S1, 96S2] and references therein).

After the fabrication of a quasi 1D channel, the actual width of the wire may be determined from a fit to the negative magneto resistance using weak-localization theory, from a fit to reproducible aperiodic conductance fluctuations or via magnetic depopulation of 1D subbands. The electron density may either be determined from Hall measurements or from Shubnikov-de-Haas oscillations. The drift mobility may be inferred from conductivity measurements and the Hall mobility from Hall measurements. The carrier concentration yields the Fermi energy and thus the Fermi velocity. Further, knowing carrier concentration and mobility, the mean free path may be derived (see for example [98T2] and references therein).

Additional information on materials, preparation and characterization may be found in any of the conference proceedings [80F, 84B1, 84C, 86B1, 86K1, 86V, 87D, 87L1, 88G1, 88L1, 88M1, 89A, 89D1, 89F1, 89L1, 89M1, 89N, 90B1, 90F1, 90K2, 91C, 91K1, 92B1, 92M1, 92P1, 93L1, 93L2, 93S, 94G2, 94I1, 95A1, 95C, 95E1, 95F1, 96B1, 96J1, 96K1, 97A1, 97S1, 97S2, 98G1].

## 6.6 References for Section 6

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